

523,666

Rec'd PCT/PTO 04 FEB 2005

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
19 February 2004 (19.02.2004)

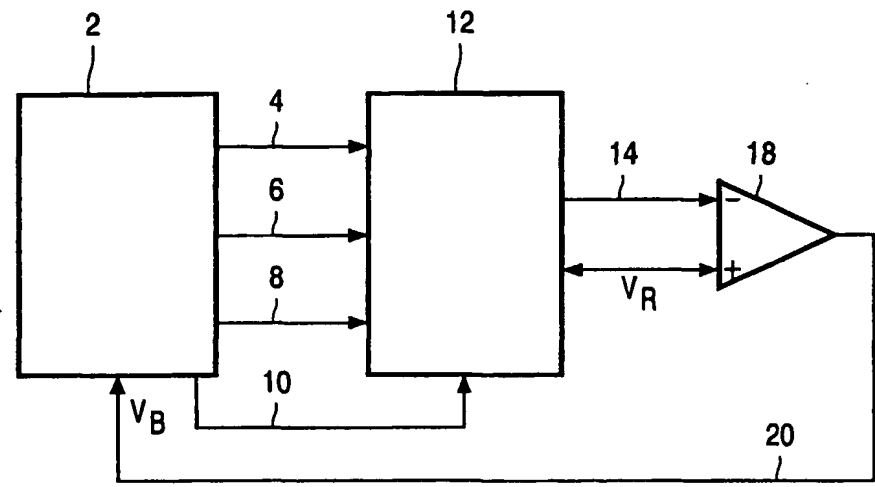
PCT

(10) International Publication Number
WO 2004/015867 A1

- (51) International Patent Classification⁷: **H03K 19/00**
- (21) International Application Number:
PCT/IB2003/003473
- (22) International Filing Date: 4 August 2003 (04.08.2003)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:
02078286.8 8 August 2002 (08.08.2002) EP
02078778.4 13 September 2002 (13.09.2002) EP
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- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).
- Published:
— with international search report
— before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

[Continued on next page]

(54) Title: **CIRCUIT AND METHOD FOR CONTROLLING THE THRESHOLD VOLTAGE OF TRANSISTORS**



(57) Abstract: A control unit controlling a threshold voltage of a circuit unit (2) having a plurality of transistor devices, comprising a reference circuit; a measuring unit (12) measuring a threshold voltage of at least one sensing transistor of the circuit unit (2) and measuring a threshold voltage of at least one reference transistor of the reference circuit; a differential voltage generator (18) generating a differential voltage from outputs of the measuring unit (12) and a bulk connection of the transistor devices in the circuit unit (2) to which the differential voltage is fed as a biasing voltage (VB).

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INTERNATIONAL SEARCH REPORT

International Application No

PCT/IB 03/03473

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 H03K19/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H03K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 397 934 A (FARRENKOPF DOUG R ET AL) 14 March 1995 (1995-03-14) column 10, line 49 -column 11, line 30; figure 4 ---	1-17
A	US 5 017 811 A (WORLEY EUGENE R) 21 May 1991 (1991-05-21) column 3, line 46 -column 4, line 17; figure 4 ---	1-17
A	US 4 791 318 A (LEFTON SCOTT ET AL) 13 December 1988 (1988-12-13) the whole document -----	1-17

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Date of the actual completion of the international search

26 November 2003

Date of mailing of the international search report

09/12/2003

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INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/IB 03/03473

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US 4791318	A	13-12-1988	NONE	